



PCIe 2.0 Compliance Test

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1 Description

The PCIe 2.0 Compliance Test for M1 Oscilloscope Tools™ will test timing and voltage parameters from the specification.

For each test section, M1 will provide a results screen containing every parameter tested and its pass/fail status, as well as the condition or value the parameter was tested against and the margin of pass or fail. Upon completion of all tests, M1 will produce a final results window summarizing the result information for every test performed. Results can be notated as needed, saved directly to a text or MS Excel format, and/or directly printed in a readable format.

If any parameter does not meet the specification, M1 will allow the user to cancel testing and bring up the measurement containing the statistic that failed to allow immediate debug and analysis.

2 General Comments

Historically, compliance specifications will contain ambiguous or contradictory statements that require a test developer to make assumptions and interpret what the spec is trying to accomplish. It is frequently the case that solution implementation decisions must be made at a level of specificity that far exceeds that which is specified, creating the possibility that different test

implementers will create solutions that are all valid implementations but which all can result in different results. ASA has discovered errors and significant ambiguities in many of the compliance specifications. Where it is felt that an assumption may have a significant bearing on the process, that assumption will be explained in this document.

3 System Requirements

3.1 Hardware and OS

Your hardware and Windows® Operating System (OS) should meet the requirements specified on the [M1 OT website](#).

3.2 M1 Version

You must have **M1 OT™ Standard** or **M1 OT™ Ultimate, v6.02.0** or higher to run this compliance test.

3.3 Oscilloscope Requirements

Please ensure you use an oscilloscope that has adequate technical specifications to perform the measurements required.

4 Installation

After downloading the .zip file containing the compliance test, extract the files in the .zip file to your M1 installation directory. If you are using Windows XP, this will be the **Shared Documents/My M1-OT/TestScripts** directory.

Some compliance tests require special measurements that are not part of M1. If there are any files with a .dll extension in the .zip file, copy those files into the **User_Defined** directory (**Shared Documents/M1 M1-OT/User_Defined**).

5 Instructions

Start M1, and turn on your scope. In M1, make sure all views are closed, then select the **Measurement Tools** menu. Click on **Compliance/TestScript**. Browse to select the desired TestScript that implements your compliance test, then click on **Play**. Note that you should select the TestScript that corresponds with whether you will be testing the transmitter (TX) or the receiver (RX). You will be prompted as to what probes to use, and what signals to probe, as the test progresses.

6 Included Measurements

The compliance test will check the following measurements from the PCI Express® Base Specification Revision 2.0. The compliance tests support both 2.5 GT/s and 5.0GT/s, with 1, 2, 4, 8, or 16 data lanes.

All table numbers refer to the tables in the specification.

6.1 Table 4-9: 2.5 and 5.0 GT/s Transmitter Specifications

- UI min, max
- $t_{20-80TX}$ min, max
- t_{skewTX} max
- $V_{cm,acTX}$ max (Gen 2i and Gen 2m)
- R/F_{bal} max (Gen 2i and Gen 2m)
- Amp_{bal} max (except for Gen 1i and Gen 1m)
- TJ at connector, 5UI (Gen 1i and Gen 1m)
- RJ at connector, 5UI (Gen 1i and Gen 1m)
- TJ at connector, 250UI (Gen 1i and Gen 1m)
- RJ at connector, 250UI (Gen 1i and Gen 1m)
- TJ at connector, $f_{BAUD}/10$ (Gen 2i and Gen 2m)
- RJ at connector, $f_{BAUD}/10$ (Gen 2i and Gen 2m)
- TJ at connector, $f_{BAUD}/500$ (Gen 2i and Gen 2m)
- RJ at connector, $f_{BAUD}/500$ (Gen 2i and Gen 2m)

6.2 Table 4-10: 5.0 GT/s Limits for Common Refclk Rx Architecture

- UI min, max
- $T_{RX-HF-RMS}$ max
- $T_{RX-HF-DJ-DD}$ max
- $T_{RX-SSC-RES}$ max
- $T_{RX-LF-RMS}$ max
- $T_{RX-MIN-PULSE}$ min
- $V_{RX-CM-CH-SRC}$ max

6.3 Table 4-11: 5.0 GT/s Tolerancing Limits for Data Clocked Rx Architecture

- UI min, max
- $T_{RX-HF-RMS}$ max
- $T_{RX-HF-DJ-DD}$ max
- $T_{RX-LF-SSC-FULL}$ max
- $T_{RX-LF-RMS}$ max
- $T_{RX-MIN-PULSE}$ min
- $V_{RX-CM-CH-SRC}$ max

6.4 Table 4-12: 2.5 and 5.0 GT/s Receiver Specifications

- UI min, max
- $V_{RX-DIFF-PP-CC}$ max
- $T_{RX-HF-DJ-DD}$ max
- $T_{RX-LF-SSC-FULL}$ max
- $T_{RX-LF-RMS}$ max
- $T_{RX-MIN-PULSE}$ min
- $V_{RX-CM-CH-SRC}$ max

7 Required Equipment

Additional equipment, such as compliance test boards, signal generators, probes, etc. may be required to perform the compliance test, as described in the relevant specification. ASA does not supply this equipment.

8 Revision History

Revision	Date	Description
1.0	17 March 2009	Updated version of test originally released with M1 OT v4

9 References

Date	Title	Author
15-February-2007	Serial ATA Revision 2.6	Serial ATA International Organization – www.serialata.org

10 Additional Resources

To download the Serial ATA TestScript for use with M1 Oscilloscope Tools, please visit:

www.M1OT.com/sata.htm

For a tutorial video on performing a compliance test with M1 Oscilloscope Tools, please visit:

www.M1OT.com/compliance-test-video.htm

To see what other compliance TestScripts ASA has provided for M1 users, please visit:

www.M1OT.com/compliance

To learn more about M1 Oscilloscope Tools, please visit:

www.M1OT.com